Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty Docket No. BRCMP007/ BP 1687 Applicant: Zheng Qi Filing Date January 8, 2002	Application No.: 10/042,019 Group 2131

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A						
	B						
	C						
	D						RECEIVED
	E						
	F						JAN 2 3 2003
	G						
	H						Technology Center 2100
	I						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation Yes	No
	J	WO 01/80483	10/25/01	PCT	H04L	9/32	X	
	K							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
ƒ ꓘ	O	"Applied Cryptography, Second Edition", Schneider, B., 1996, John Wiley & Sons, New York, XP002184521, cited in the application, page 442, paragraph 18.7 – page 445.
ƒ ꓘ	P	"SHA: The Secure Hash Algorithm Putting Message Digests to Work", Stallings, W., Dr. Dobbs Journal, Redwood City, CA, 1 April 1994, Page 32-34, XP000570561.
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		9/22/2003

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Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation Yes	No
RA	A	WO 0/80483 A	25.10.01	PCT			X	

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Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
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F A	C	Stallings W., "SHA: The Secure Hash Algorithm Putting Message Digests to Work", Dr. Dobbs Journal, Redwood City, CA, US, 1 April 1994 (1994-04-01), XP00570561, page 32-33.

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FA	A9	C. Madson, R. Glenn: "RFC 2403- The Use of HMAC-MD5-96 within ESP and AH", IETF Request for Comments, November 1998, XP002163402, Retrieved from Internet on 3/20/01, http://www.faqs.org/rfcs/frc2403.html, 87 Pages.
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FA	A12	Pierson, et al., "Context-Agile Encryption for High Speed Communication Networks", Computer Communications Review, Association for Computing Machinery, Vol. 29, No. 1, January 1999, pages 35-49.
FA	A13	Sholander, et al., "The Effect of Algorithm-Agile Encryption on ATM Quality of Service", Global Telecommunications Conference (Globecom), IEEE, 3 November 1997, pages 470-474.
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	B3	Wassal, et al., "A VLSI Architecture for ATM Algorithm-Agile Encryption", Proceedings Ninth Great Lakes Symposium on VLSI, 4-6 March 1999, pages 325-328.
	B4	Analog Devices: "Analog Devices and IRE Announce First DSP-Based Internet Security System-On-A-Chip", Analog Devices Press Release, Online, 19 January 1999, pages 1-3. http://content.analog.com/pressrelease/prdisplay/0,1622,16,00.html.
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	B9	Senie, D., "NAT Friendly Application Design Guidelines", Amaranth Networks, Inc., NAT Working Group, Internet-Draft, September 1999, pp. 1-7.
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ℐℒ	C5	Srisuresh, P., "Security Model with Tunnel-mode Ipsec for NAT Domains", Lucent Technologies, Network Working Group, October 1999, pp. 1-11.
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ℐℒ	C8	Kent, S., et al., "Security Architecture for the Internet Protocol", Network Working Group, November 1998, pp. 1-66.
ℐℒ	C9	"Compression for Broadband Data Communications", BlueSteel Networks, Inc., September 8, 1999, pp. 1-9.
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